



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/217,213

Filed: December 21, 1998

Inventors:

Mark I. Gardner

H. Jim Fulford

Derick J. Wristers

Title: Semiconductor Fabrication  
Employing Implantation of  
Excess Atoms at the Edges of a  
Trench Isolation Structure

Examiner: Mai, Anh D.

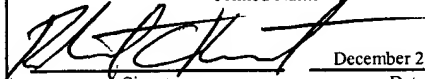
Group/Art Unit: 2814

Atty. Dkt. No: 5500-05001

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Robert C. Kowert

Printed Name

  
Signature

December 21, 2004

Date

**NOTICE OF CHANGE OF ADDRESS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

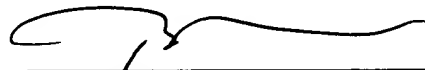
Dear Sir:

Applicants respectfully request the Commissioner to change the correspondence address for the above identified patent application as described below.

|              |   |
|--------------|---|
| Old Address: | Kevin L. Daffer<br>Conley, Rose & Tayon, P.C.<br>P.O. Box 3267<br>Houston, Texas 77253-3267<br>(512) 476-1400 |
|--------------|---|

|                 |   |
|-----------------|---|
| New Address:    | Robert C. Kowert<br>Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C.<br>P.O. Box 398<br>Austin, Texas 78767-0398 |
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Respectfully submitted,



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Date: December 21, 2004